

L Number	Hits	Search Text	DB	Time stamp
2	2066	(710/8-14).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 07:57
3	6426	(714/25-46).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 07:58
1	290	703/21.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:42
4	8695	703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:42
5	9947	DUT or (device adj under adj test)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:45
6	281891	simulat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:46
7	25984	debug\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:47
8	88384	verificat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:47
9	37347	emulat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:47
10	1053307	peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:48
11	273	(703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:49
12	273	(DUT or (device adj under adj test)) and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:49
13	94	simulat\$4 and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:49
14	43	debug\$4 and (simulat\$4 and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:50
15	21	verificat\$4 and (debug\$4 and (simulat\$4 and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:50
16	9	emulat\$4 and (verificat\$4 and (debug\$4 and (simulat\$4 and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:51
17	3	peripheral and (emulat\$4 and (verificat\$4 and (debug\$4 and (simulat\$4 and ((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device adj under adj test))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/07/15 09:51

L Number	Hits	Search Text	DB	Time stamp
1	0	simulat\$4 and (microprocessor or micro near processor or processor) same clock and simulat\$5 and debug\$6 and perl and BIOS and DOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:34
2	0	simulat\$4 same (microprocessor or micro near processor or processor) and clock and debug\$6 and perl and BIOS and DOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:34
3	0	simulat\$4 same (microprocessor or micro near processor or processor) and clock and debug\$6 and perl and BIOS and DOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:34
4	0	simulat\$4 same (microprocessor or micro near processor or processor) and debug\$6 and BIOS and DOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:35
5	0	simulat\$4 same (microprocessor or micro near processor or processor) and debug\$6 and BIOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:35
-	85	simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 17:27
-	29	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:24
-	28	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) or ((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8)) and verilog	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:04
-	2	"US 20020107678 A1"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:05
-	14	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and processor near3 clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:49
-	3	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and (register near transfer near level or rtl) near2 model and gate near2 model	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:52
-	13	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and (register near transfer near level or rtl) near2 model	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:53

-	15	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and (register near transfer near level or rtl)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 13:53
-	1	simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6 and motif near2 library	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 16:48